

Prof BS Saini

From: "Placement Punjabi University" <placement@pbi.ac.in>
Date: Thursday, February 08, 2018 9:59 PM
To: "Balraj Singh" <placement.brs@gmail.com>
Cc: "Piyush Malhotra" <pnmalhotra201295@gmail.com>; "Meghna Kapoor" <meghnakapoor1234@gmail.com>; "Manpreet Kaur" <manpreetkaur6136@gmail.com>; "Preet Gill" <gillpreet0996@gmail.com>; "Prabal Sharma" <prabal.sharma86@gmail.com>
Attach: data format (7).xls
Subject: Fwd: Invitation to a Joint Campus Placement Drive by SoCtronics Technologies at CGC

Dr Balraj - pl post for B.Tech (ECE) - details as per trailing mail.

Interested students to send their details, as per the attached Format, by **9th** February to <placement@pbi.ac.in> with 'SCT' as the subject.

Dr Dham
 Placement Cell PUP

----- Forwarded message -----

From: AVNISH RAO <invitation@cgc.edu.in>
Date: Thu, Feb 8, 2018 at 3:57 PM
Subject: Fwd: Invitation to a Joint Campus Placement Drive by SoCtronics Technologies at CGC
To:

Dear Sir/Madam,

Chandigarh Group of Colleges takes this opportunity to invite students from your campus to a joint campus placement drive by **SoCtronics Technologies Pvt. Ltd/ Invecas Technologies Pvt Ltd** for **B.Tech/M.Tech (ECE, EE, EEE)** students of 2018 passing out batch. The details are as mentioned below.

About the Company

SoCtronics is a customer-focused VLSI design services company offering complete Spec-to-Silicon turnkey solutions in Logic Design, Physical Design, Analog Mixed Signal Design, Circuit Layout , Embedded System Design and System Engineering. SoCtronics employs proven methodologies and established processes in their design flow and has a consistent track record of first time Silicon Success. Their success stories include completing turnkey projects covering all aspects of specification to chip and many successful tape-outs for their high-technology clientele located in USA, Canada, EU, Israel, Japan, China and Taiwan and also their subsidiary design centres in India. SoCtronics is led by experienced senior executives, who have vast & rich experience in the VLSI field over decades of association with the Silicon Valley, USA. (www.soctrionics.com)

Company Name	SoCtronics Technologies Pvt. Ltd. / Invecas Techn
Date of Drive	24th February 2018
Venue	Chandigarh Group of Colleges

	Landran (Kharar-Banur Road) Mohali
Stream Required	<ul style="list-style-type: none"> • B.Tech (ECE, EE, EEE) • M.Tech (ECE, EE, EEE)
Eligibility criteria	70% in B.Tech / M.Tech
Batch	2018
Job Profile and Position	<p><u>Analog Design</u></p> <ul style="list-style-type: none"> • Designing of Foundation libraries (IOs , Memory) • AMS IPs such as PLL , ADC/DAC, Power Management • Serial Links such as USB , SATA, PCIe, HDMI , DP etc. <p><u>Logic Design / Physical Design</u></p> <ul style="list-style-type: none"> • RTL • Design synthesis • DFT • Design verification • Place and Route etc. <p><u>Full Custom Layout Design</u></p> <ul style="list-style-type: none"> • Mask design of Analog IPs and custom circuits • Physical verification
Job location	Noida primarily
Pay package	<p><u>Analog Design :</u></p> <ul style="list-style-type: none"> • <u>Salary 1st year : 4.5 – 5 LPA</u> • <u>From 2nd year onwards : 6 - 6.5 LPA</u> <p><u>Logic Design / Physical Design :</u></p> <ul style="list-style-type: none"> • <u>Salary 1st year : 3 – 3.6 LPA</u> • <u>From 2nd year onwards : 4.5 - 5 LPA</u> <p><u>Full Custom Layout Design :</u></p> <ul style="list-style-type: none"> • <u>Salary 1st year : 3 – 3.6 LPA</u> • <u>From 2nd year onwards : 4.5 - 5 LPA</u> <p><u>Note:</u></p> <ul style="list-style-type: none"> • The package mentioned above is CTC. As per the structure today- it includes medi-claim insurance,

	<p>employer contribution to PF and gratuity. Rest is payable after applicable taxes.</p> <ul style="list-style-type: none"> • No variable component is included in CTC
Any bond or fee	Selected students would need to provide a 1.5 Lac bank guarantee for 2 year and 3.5 L bond agreement for next 2 yrs stay with company after training.
Placement process	<ul style="list-style-type: none"> • PPT • Online Test • Written Test • Direct Interview (Whole process will complete within Two days)
<p>Concerned TPOs are requested to forward the list of eligible students for the exam latest by 10th February 2018 at crc@cgc.edu.in and register your college at the below link:</p> <p>TPO Registration : http://vedaiit.org/application/veda_examination/tpo/tpo_registration.php</p> <p>TPO Login : http://vedaiit.org/application/veda_examination/tpo_login.php</p> <p>The company will open the online registration link for students once the needed details are available.</p>	

You are requested to intimate all the students of 2018 passing out batch accordingly and forward the **list** of registered students, as per the attached format, at crc@cgc.edu.in latest by **10th February 2018** positively.

For queries please feel free to contact us at: **9781925274, 0172-3984241.**

Thanks and Regards

Avnish K. Rao

Asstt. Manager-Placement

Chandigarh Group of Colleges

Landran (Kharar-Banur Road) Mohali

